

Remarks

The foregoing amendments and following remarks are responsive to the March 24, 2005 Office Action. Applicants respectfully request reconsideration and withdrawal of the rejections.

Status of the Claims

Claims 13, 15, 16, and 19 are amended. Claims 22-25 are added. Claims 1-12 and 20 were cancelled previously. The pending claims are now Claims 13-19 and 21-25.

Support for Amended Specification

The specification is amended to correct typographical errors. Support is found in the specification on page 19, line 3 and in FIGS. 8D, 8G-8I. No new matter is added.

Support for Amended and Appended Claims

Support for amended Claims 13 and 16 is found throughout the specification, and on page 4, lines 11-20, page 5, lines 30-31, and page 6, lines 4-5. Claims 15 and 19 are amended for antecedent purposes. New Claim 22 is added to cover the subject matter cancelled from Claim 19. Support for new Claims 23-25 is found throughout the specification, and on page 5, lines 8-14 and page 12, line 18 to page 13, line 15. No new matter is added.

Rejection under 35 U.S.C. § 102(b)

Claims 13-19 and 21 were rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 6,266,226 to Hayashi (the Hayashi patent). Claims 13 and 16 have been amended to clarify the invention.

The Hayashi patent relates to a monolithic microwave integrated circuit (MMIC) surface mount capacitor that includes comb-type electrodes formed on a ceramic substrate as illustrated in FIGS 1A-2B. In all embodiments, a lower and an upper electrode (each having a plurality of electrode elements) are formed on opposing sides of a dielectric layer. The lower electrode may be a copper film formed by sputtering on the ceramic substrate, or coating or printing a thick film conductive paste thereon. The film is formed into a comb-type electrode using photolithography technology.

Applicants' invention relates to high tolerance embedded capacitors for use in printed wiring boards. According to an aspect of the invention, a capacitor includes a first electrode comprising a plurality of electrode portions; a first dielectric disposed over the first electrode;

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and a second electrode formed from a metallic foil and disposed over the first dielectric. According to another aspect of the invention, a capacitor includes a first electrode comprising a first plurality of electrode portions; a second electrode comprising a second plurality of electrode portions spaced from and interdigitated with the first plurality of electrode portions, wherein the first and second electrodes are formed from a metallic foil; and a dielectric disposed between the first and second pluralities of electrode portions.

The capacitors of the Hayashi patent are formed on a ceramic substrate for a surface mount device whereas the Applicants' capacitors are formed on a metallic foil for use in a printed wiring board. None of the embodiments of the Hayashi patent disclose, teach or suggest a capacitor on which any of the electrodes are formed from a metallic foil. Since the Hayashi patent fails to disclose each and every element of the invention, the rejection should be withdrawn. Accordingly, reconsideration and withdrawal of the rejection is respectfully requested.

Citation of References

The references cited but not relied upon by the Examiner have been reviewed and a detailed discussion appears unnecessary.

Fees

A fee for a one-month extension of time to accompany the Petition for a One-Month Extension of Time is enclosed. No additional fees are believed due. The Commissioner is authorized, however, to charge (or credit any balance) any fees deemed due (or owing) to Deposit Account 04-1928 (E. I. du Pont de Nemours and Company).

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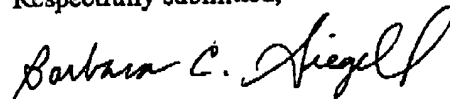
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Conclusion

It is respectfully submitted that Claims 13-19 and 21-25 are in condition for allowance. A Notice of Allowance is respectfully requested. If anything further is needed to advance the allowance of this application, the Examiner is urged to contact Applicants' attorney at the telephone number below.

Respectfully submitted,



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